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U.S. UTILITY Patent Application

PATENT NUMBER and
ISSUE DATE

306572

APPL. NUM. 10063376	FILING DATE 04/17/2002	CLASS 257	SUBCLASS 530	GAU 2811	EXAMINER <i>Lolce</i>
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**APPLICANTS: Radens, Carl; Tonti William;

**CONTINUING DATA VERIFIED:

none
Lolce

** FOREIGN APPLICATIONS VERIFIED:

none
Lolce

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>	ATTORNEY DOCKET NO.
Foreign priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no		35 USC 119 conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no	
Verified and Acknowledged Examiners's initials <i>Lolce</i>		BUR920010105	

TITLE : MOS antifuse with low post-program resistance

U.S. DEPT. OF COMM./PAT. & TM-PTO-436 (Rev. 12-94)

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for O.G.
Assistant Examiner		DRAWING	
Amount Due	Date Paid	Sheets Drwg.	Figs. Drwg.
		Print Fig.	
Primary Examiner		Application Examiner	
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